

Team Name: sdmay24-25

Team Members: Thomas Gaul, Matthew Crabb, Spencer Sutton, Tori Kittleson, Ian Hollingworth

Report Period: Aug 28-Sept 10

Summary of Progress in this Period

Over the first period of this project, we started to get acclimated with the project; we had our first two meetings with our advisor and client, Professor Duwe, and started to understand the requirements. For our next meeting, our team has compiled a list of our understanding requirements to ensure we have a common understanding with Duwe and the graduate students who will use it.

Another progress area we have made these first two weeks is getting access to things. We got access to the research Teams and have a channel for our own use. Additionally we got access to the building and lab that this research project makes use of. We got access to the github to see the code to build our own work off of. Finally, we got physical boards our board will have to interface with and our code needs to run on.

In terms of actual work we completed on the project we started to download the tools we need to use and familiarize ourselves with them. For starters we installed KiCAD and did a basic schematic and layout design to familiarize ourselves with it. Then we installed CCS which will be used for the code development for our project. Finally we assigned the other project boards to members to make schematic symbols and corresponding footprints for use in our future PCB designs.

Pending Issues

Currently, we have a few issues that our team needs to resolve. The first of which is a lack of overall understanding of the project, which we should be able to solidify as we start work and have meetings. The other issue is figuring out our task organization system for the team to have assignments.

Plans for Upcoming Reporting Period

Our plan for the next reporting period is to finish the previously mentioned symbols and footprints and to complete the schematic for the BOB PCB. This PCB is supposed to be an easier introductory board, replacing a disorganized set of wires that are currently used. We want to be able to complete more coding tasks, but also need to understand more to define these plans outside of setting up our own code repository, having the IDE set up, and exploring the existing code.